

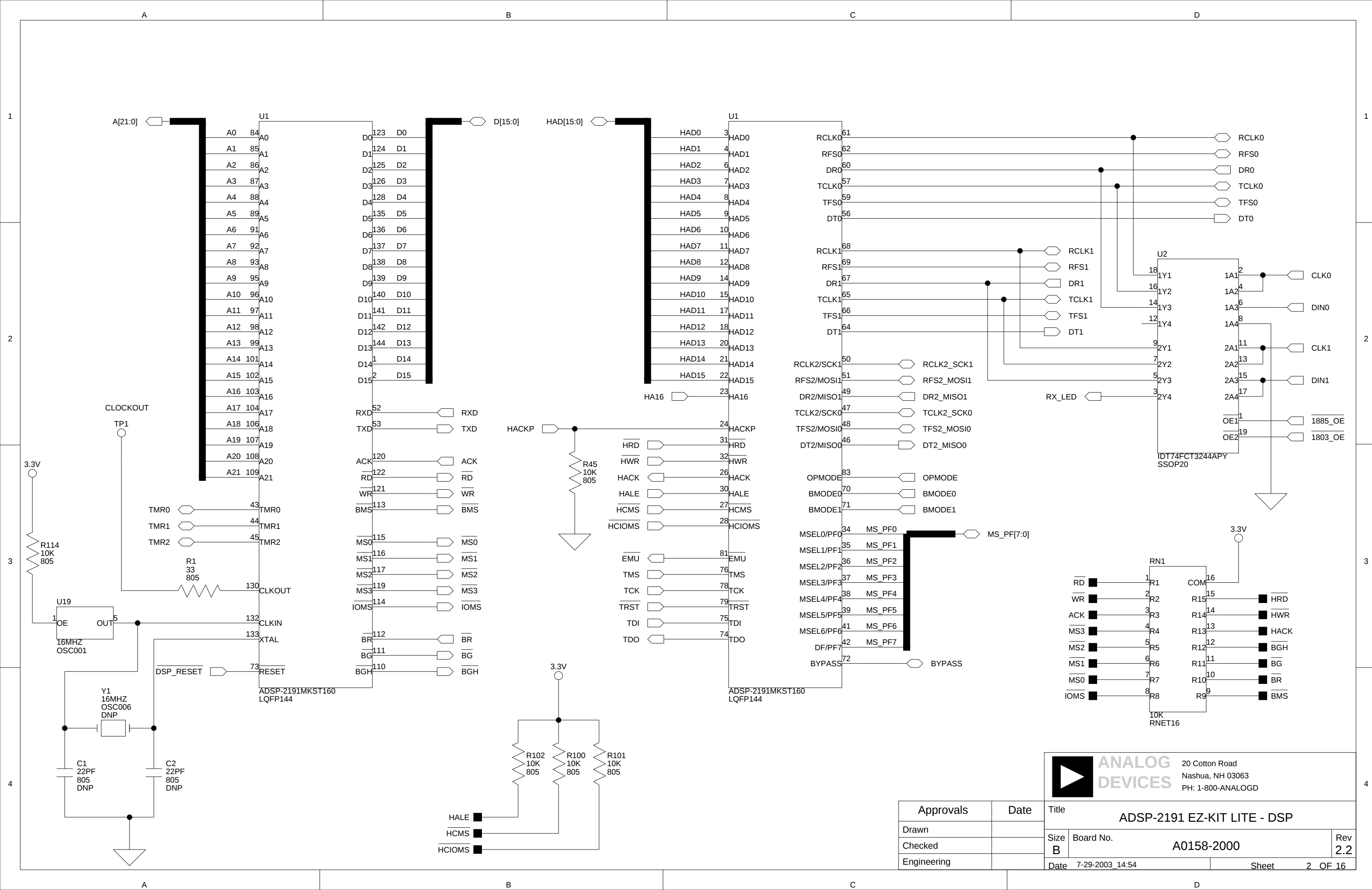
ADSP-2191 EZ-KIT LITE

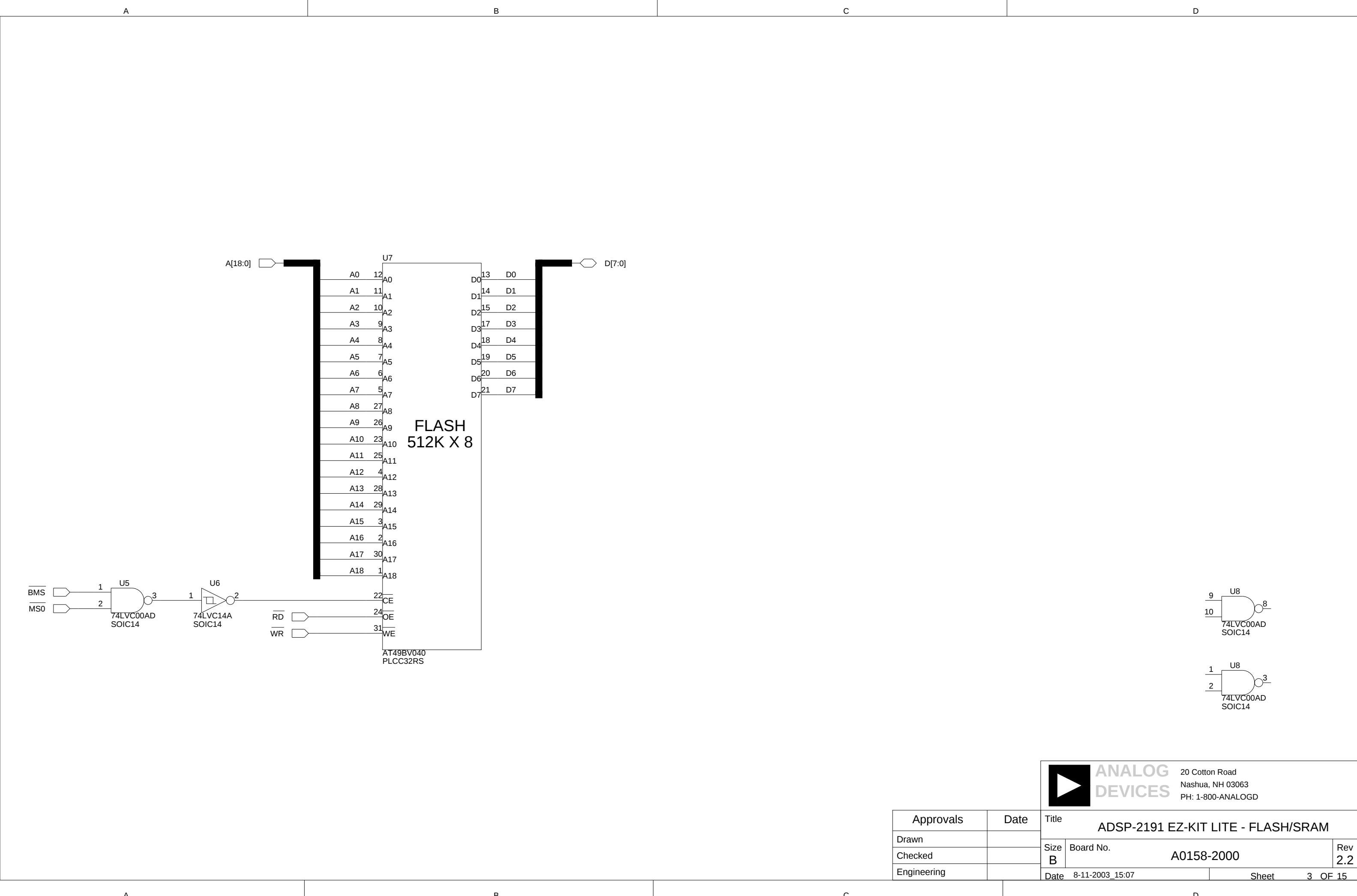


ANALOG
DEVICES

20 Cotton Road
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PH: 1-800-ANALOGD

Title		ADSP-2191 EZ-KIT LITE - TITLE	
Size	Board No.	Rev	
B	A0158-2000	2.2	
Date	7-16-2002 13:57	Sheet	1 OF 16





		ANALOG DEVICES		20 Cotton Road Nashua, NH 03063 PH: 1-800-ANALOGD	
Title		ADSP-2191 EZ-KIT LITE - FLASH/SRAM			
Size B		Board No. A0158-2000			Rev 2.2
Date		8-11-2003_15:07		Sheet	3 OF 15

Approvals	Date
Drawn	
Checked	
Engineering	

JP1 SETTINGS		
SJ1	SJ2	INPUT SOURCE
1 & 3	2 & 4	MIC IN
3 & 5	4 & 6	LINE IN
* = DEFAULT		

SJ2
SHORTING
JUMPER
DEFAULT=3 & 5

SJ1
SHORTING
JUMPER
DEFAULT=4 & 6

LINE IN/MIC

HEAD PHONE OUT

D15 VC0805 40A 805
D14 VC0805 40A 805
D13 VC0805 40A 805
D12 VC0805 40A 805

POPULATE TO COMPLY WITH PC99

NOTE: Place MIC2 TestPoint(TP2) as close to PIN (22) as possible.

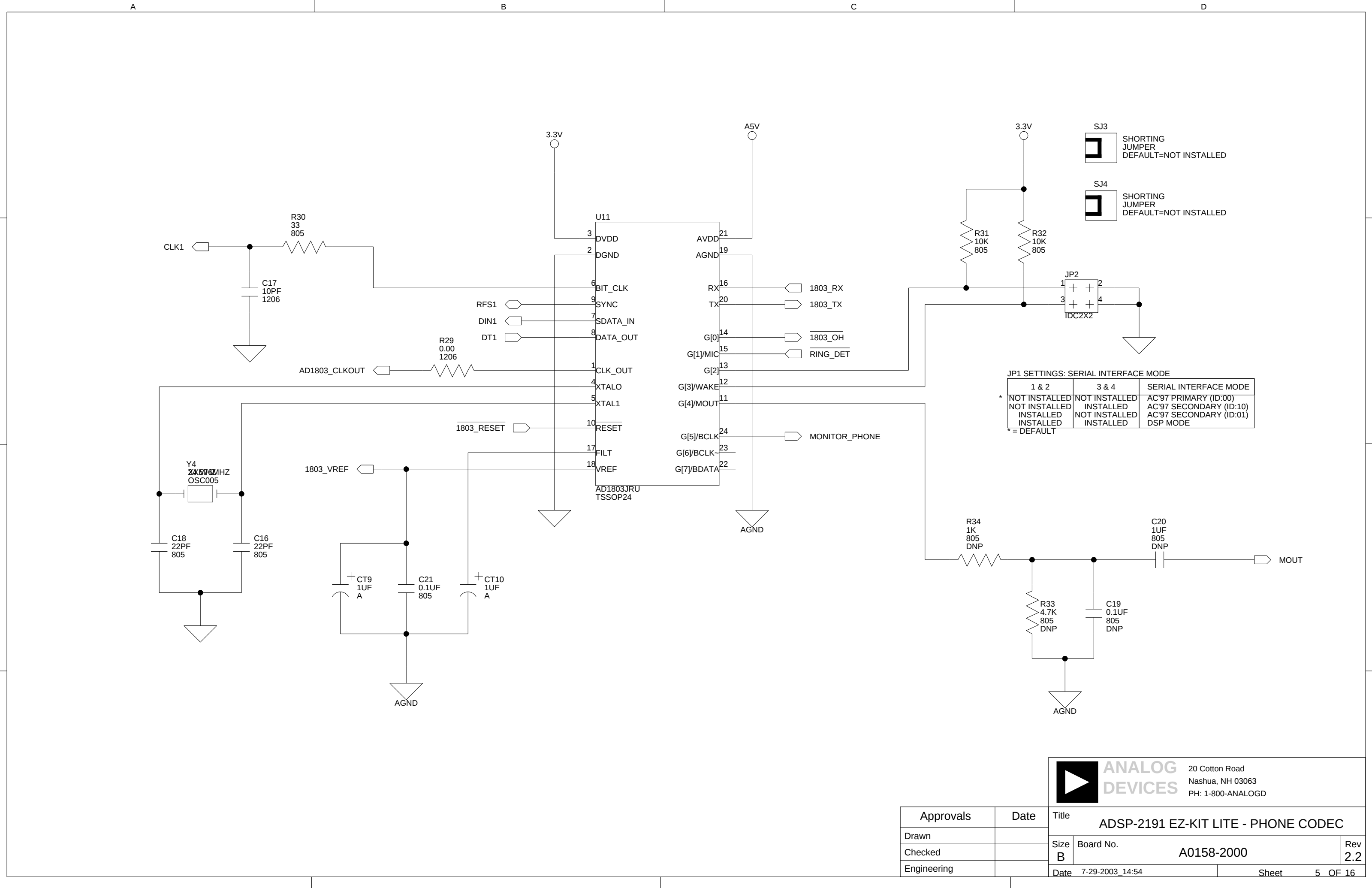
AD1885



ANALOG
DEVICES

20 Cotton Road
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Approvals		Date	Title	
Drawn			ADSP-2191 EZ-KIT LITE - STEREO CODEC	
Checked				
Engineering				
Size B		Board No.	A0158-2000	
Date		7-29-2003_14:54	Sheet	4 OF 16
			Rev	2.2



JP1 SETTINGS: SERIAL INTERFACE MODE

1 & 2	3 & 4	SERIAL INTERFACE MODE
* NOT INSTALLED	NOT INSTALLED	AC'97 PRIMARY (ID:00)
NOT INSTALLED	INSTALLED	AC'97 SECONDARY (ID:10)
INSTALLED	NOT INSTALLED	AC'97 SECONDARY (ID:01)
INSTALLED	INSTALLED	DSP MODE

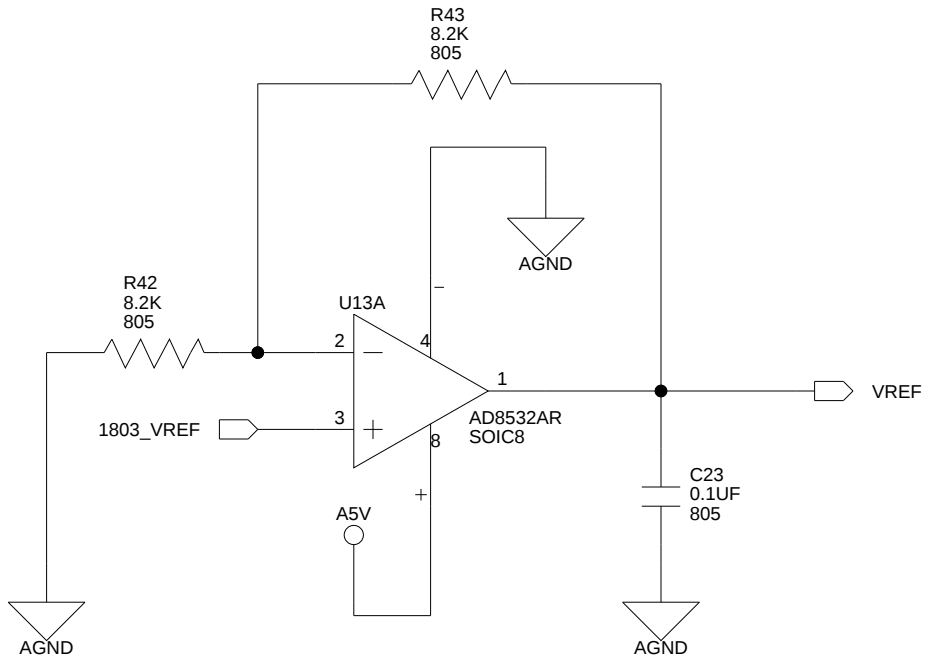
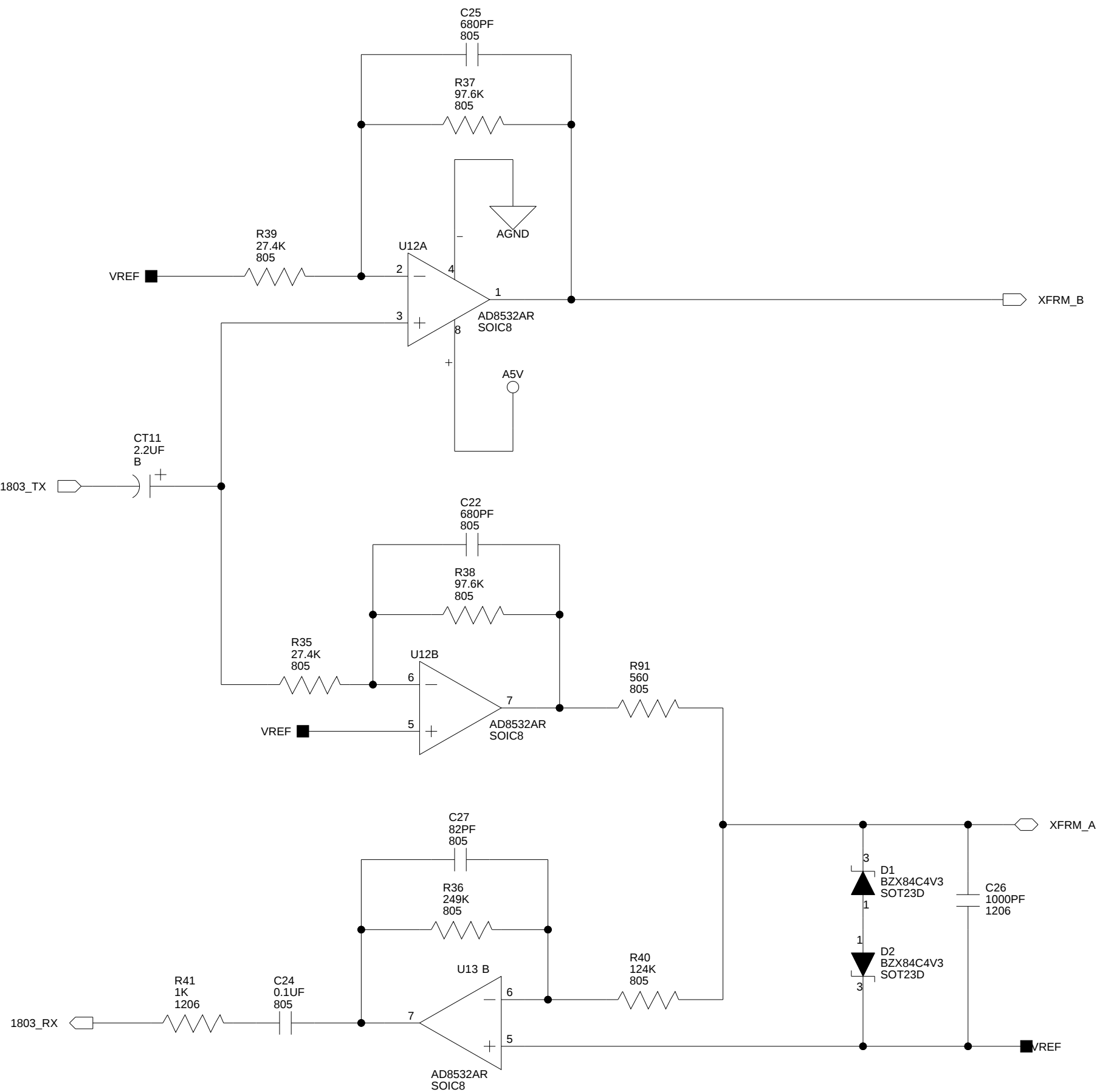
* = DEFAULT



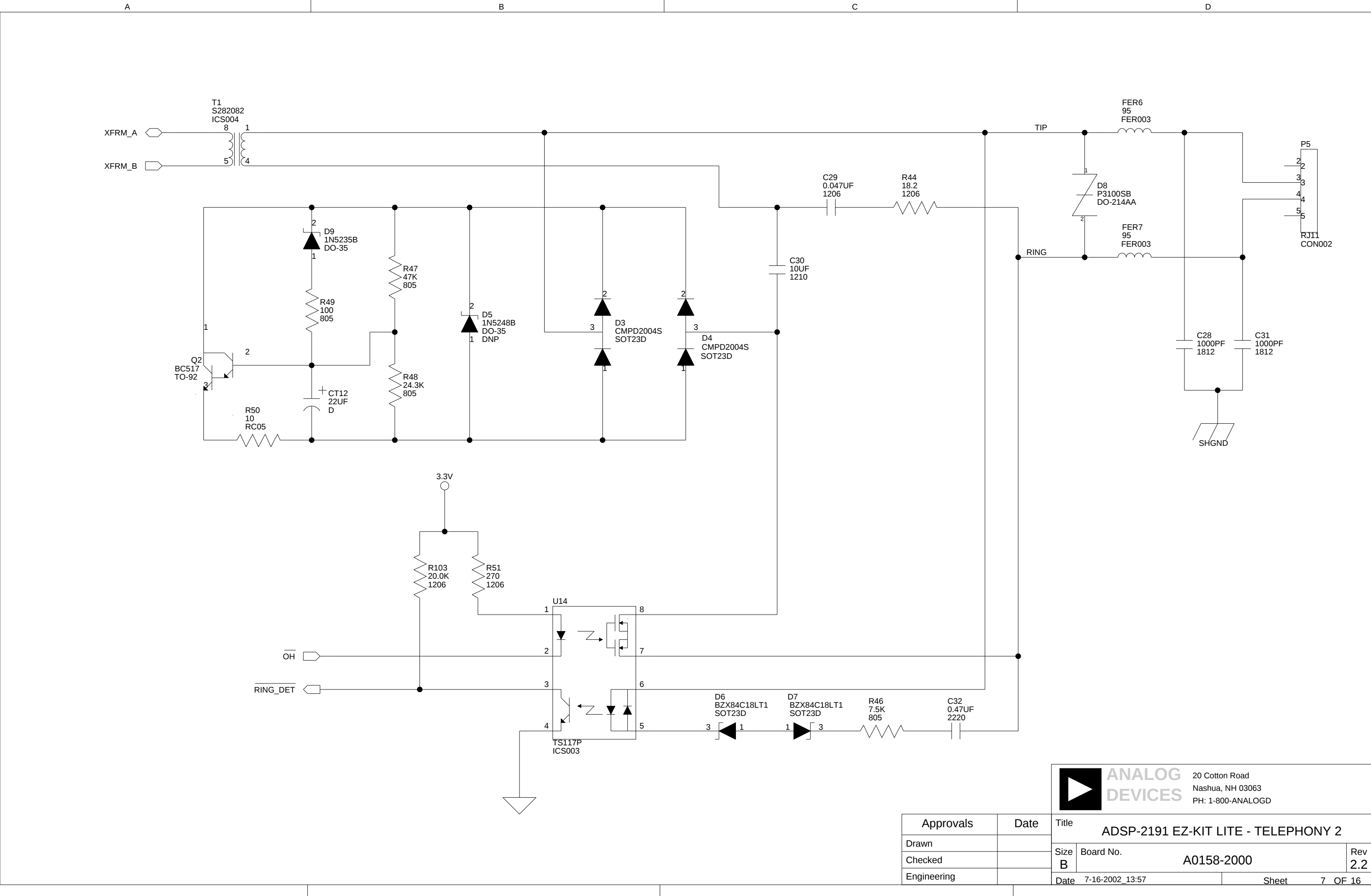
**ANALOG
DEVICES**

20 Cotton Road
Nashua, NH 03063
PH: 1-800-ANALOGD

Approvals		Date	Title		
Drawn			ADSP-2191 EZ-KIT LITE - PHONE CODEC		
Checked			Size B	Board No. A0158-2000	Rev 2.2
Engineering			Date 7-29-2003_14:54	Sheet 5	OF 16



Approvals		Date	Title ADSP-2191 EZ-KIT LITE - TELEPHONY 1		
Drawn			Size B	Board No. A0158-2000	Rev 2.2
Checked					
Engineering			Date 7-16-2002_13:57	Sheet 6	OF 16



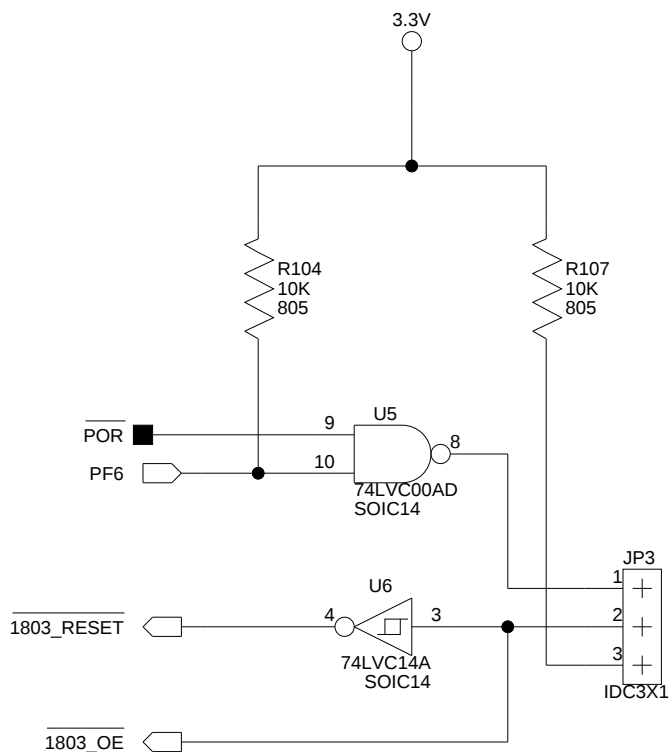
**ANALOG
DEVICES**

20 Cotton Road
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Approvals	Date
Drawn	
Checked	
Engineering	

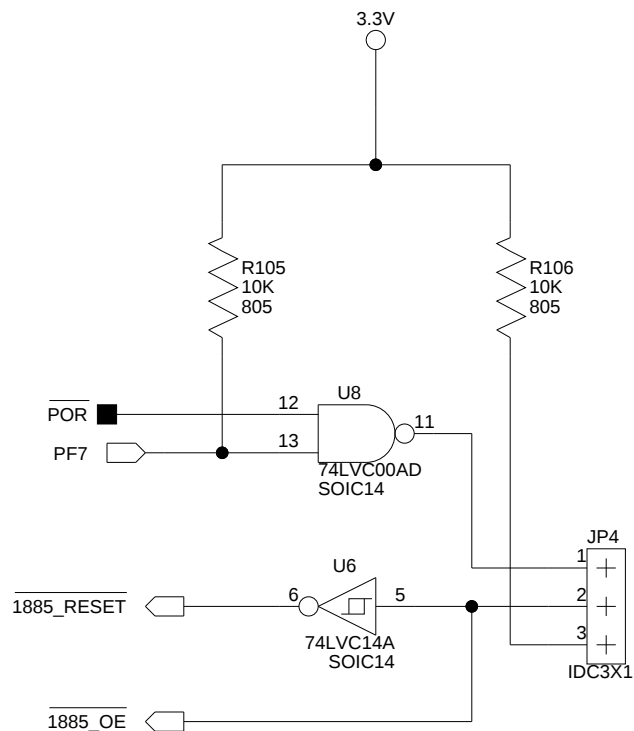
Date	7-16-2002_13:57
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Title			
ADSP-2191 EZ-KIT LITE - TELEPHONY 2			
Size	Board No.		Rev
B	A0158-2000		2.2
Date	7-16-2002 13:57		Sheet 7 OF 16



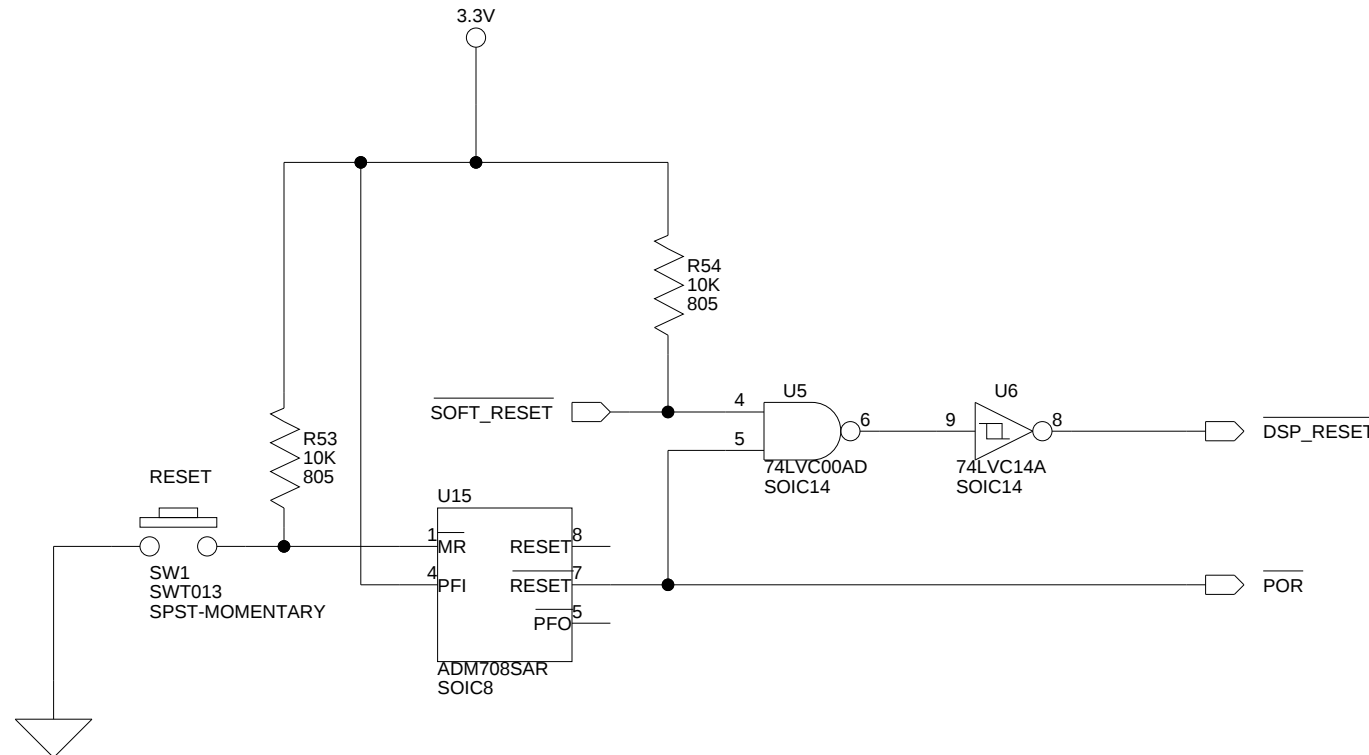
JP3: 1803 DISABLE
* 1 & 2 NORMAL OPERATION
2 & 3 DISABLE AD1885
* = DEFAULT

SJ5
SHORTING
JUMPER
DEFAULT=1 & 2

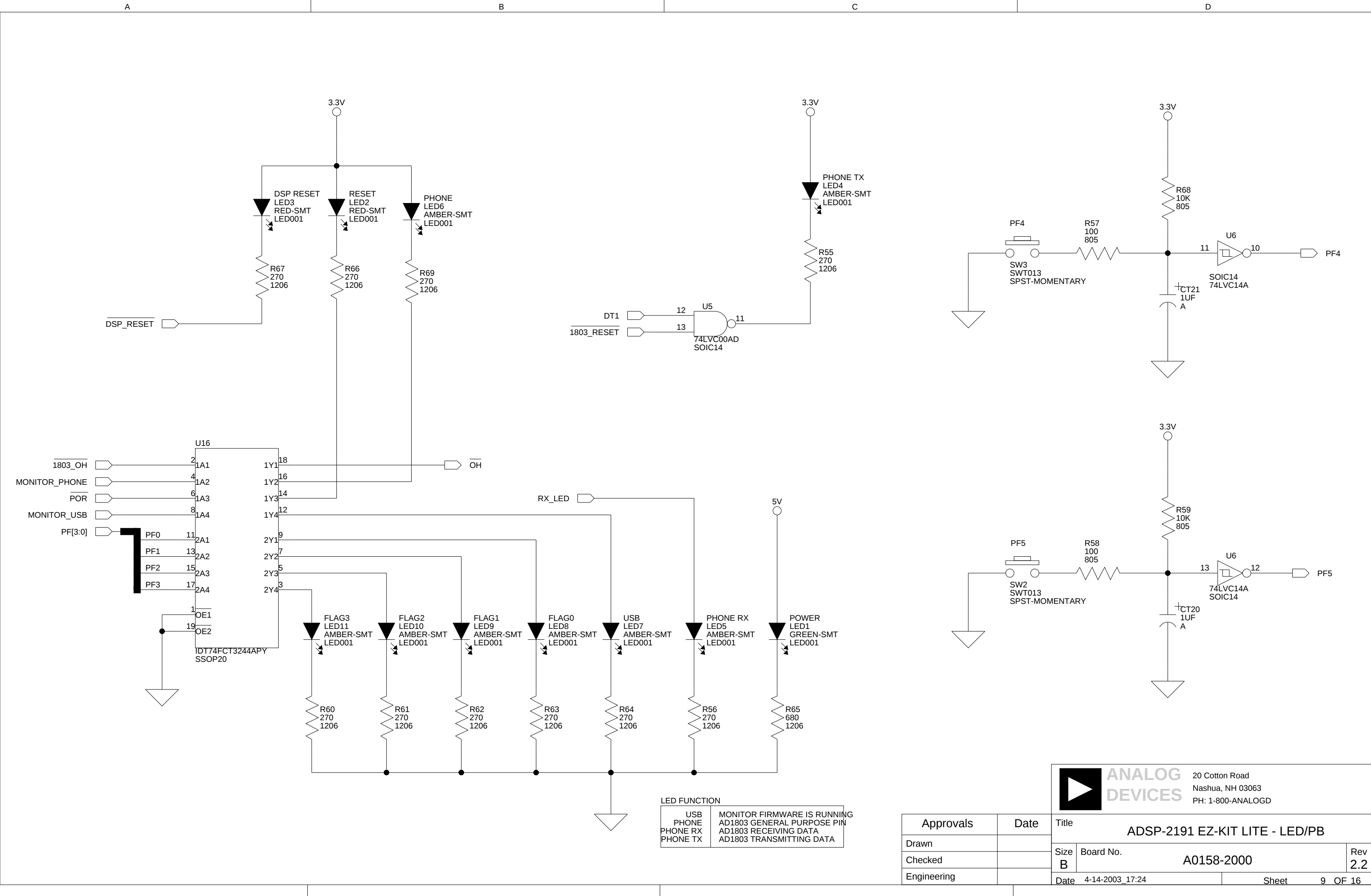


JP4: 1885 DISABLE
* 1 & 2 NORMAL OPERATION
2 & 3 DISABLE AD1885
* = DEFAULT

SJ6
SHORTING
JUMPER
DEFAULT=1 & 2



Approvals		Date	Title		
Drawn			ADSP-2191 EZ-KIT LITE - RESET		
Checked			Size B	Board No. A0158-2000	Rev 2.2
Engineering			Date 7-16-2002_13:57	Sheet 8	OF 16



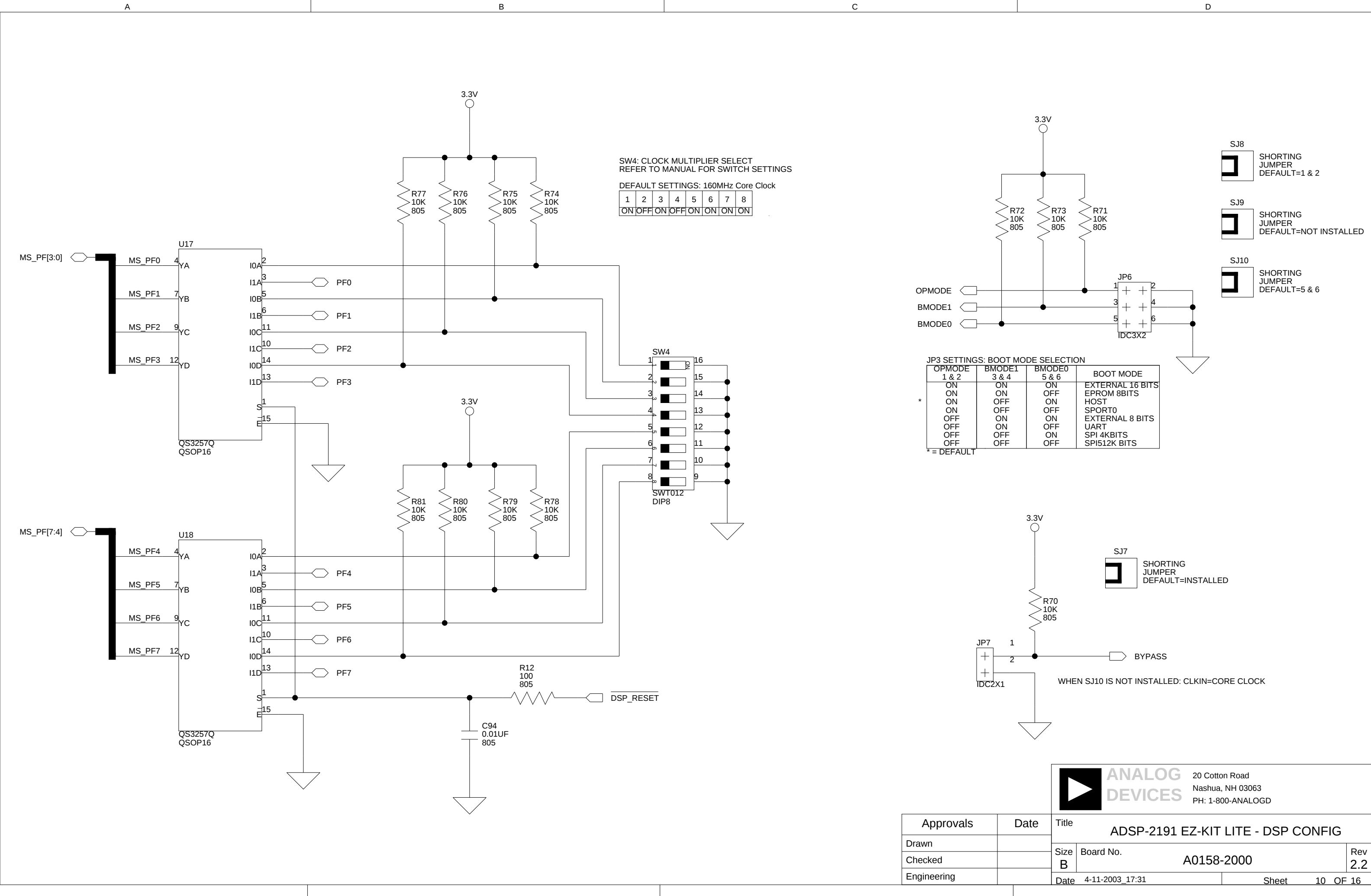
LED FUNCTION	
USB	MONITOR FIRMWARE IS RUNNING
PHONE	AD1803 GENERAL PURPOSE PIN
PHONE RX	AD1803 RECEIVING DATA
PHONE TX	AD1803 TRANSMITTING DATA

Approvals	Date
Drawn	
Checked	
Engineering	

Title		ADSP-2191 EZ-KIT LITE - LED/PB	
Size	Board No.	A0158-2000	
B		Rev 2.2	
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 **ANALOG
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SW4: CLOCK MULTIPLIER SELECT
REFER TO MANUAL FOR SWITCH SETTINGS

DEFAULT SETTINGS: 160MHz Core Clock

1	2	3	4	5	6	7	8
ON	OFF	ON	OFF	ON	ON	ON	ON

JP3 SETTINGS: BOOT MODE SELECTION

OPMODE 1 & 2	BMODE1 3 & 4	BMODE0 5 & 6	BOOT MODE
ON	ON	ON	EXTERNAL 16 BITS
ON	ON	OFF	EPROM 8BITS
ON	OFF	ON	HOST
ON	OFF	OFF	SPORT0
OFF	ON	ON	EXTERNAL 8 BITS
OFF	ON	OFF	UART
OFF	OFF	ON	SPI 4KBITS
OFF	OFF	OFF	SPI512K BITS

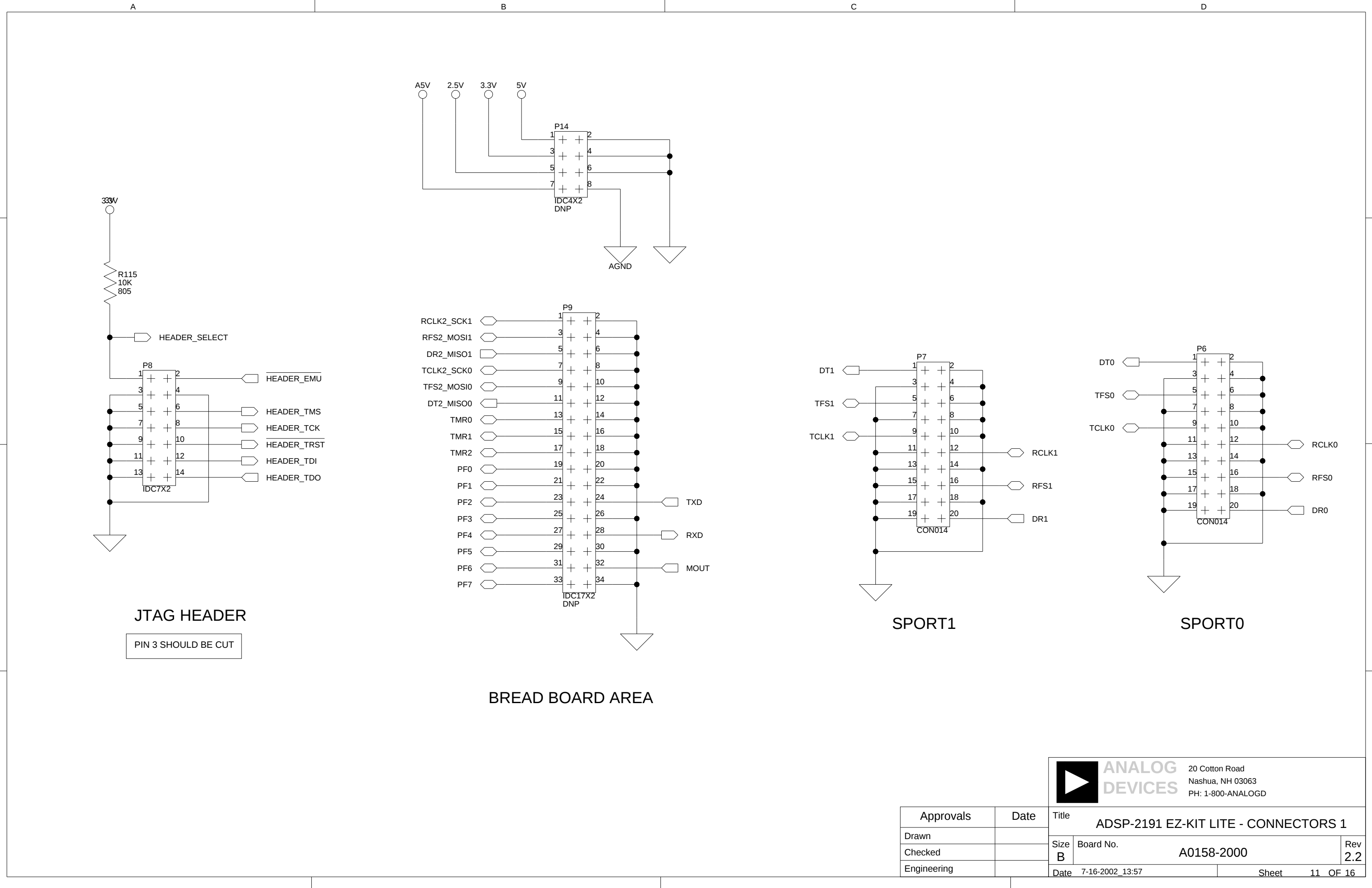
* = DEFAULT



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Approvals		Date	Title		
Drawn			ADSP-2191 EZ-KIT LITE - DSP CONFIG		
Checked					
Engineering			Size B	Board No. A0158-2000	Rev 2.2
			Date	4-11-2003_17:31	Sheet 10 OF 16





**ANALOG
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20 Cotton Road
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Title ADSP-2191 EZ-KIT LITE - CONNECTORS 1		Size B	Board No. A0158-2000	Rev 2.2
Date 7-16-2002_13:57		Sheet 11 OF 16		

Approvals	Date
Drawn	
Checked	
Engineering	

1

1

2

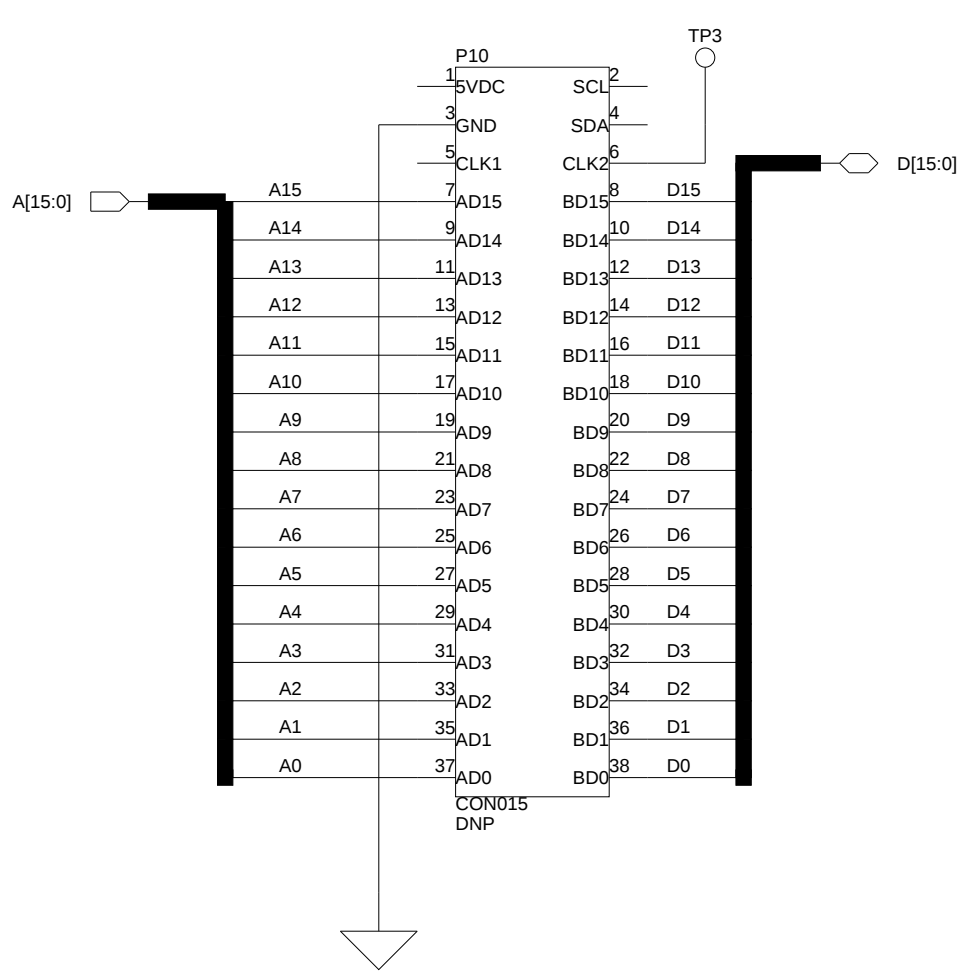
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3

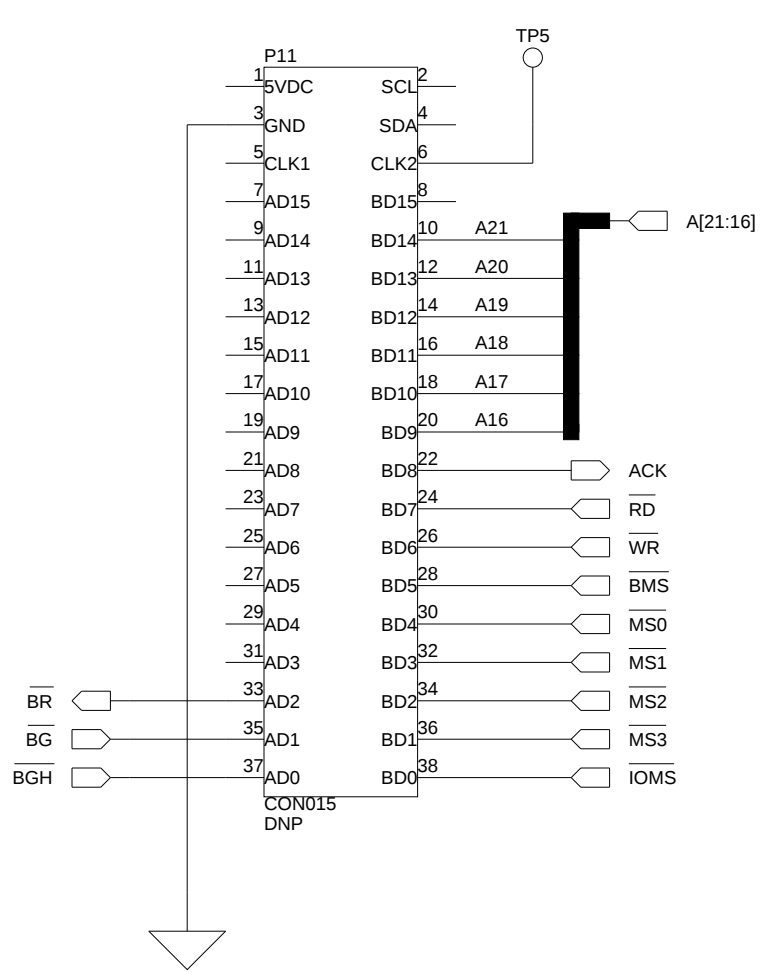
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4

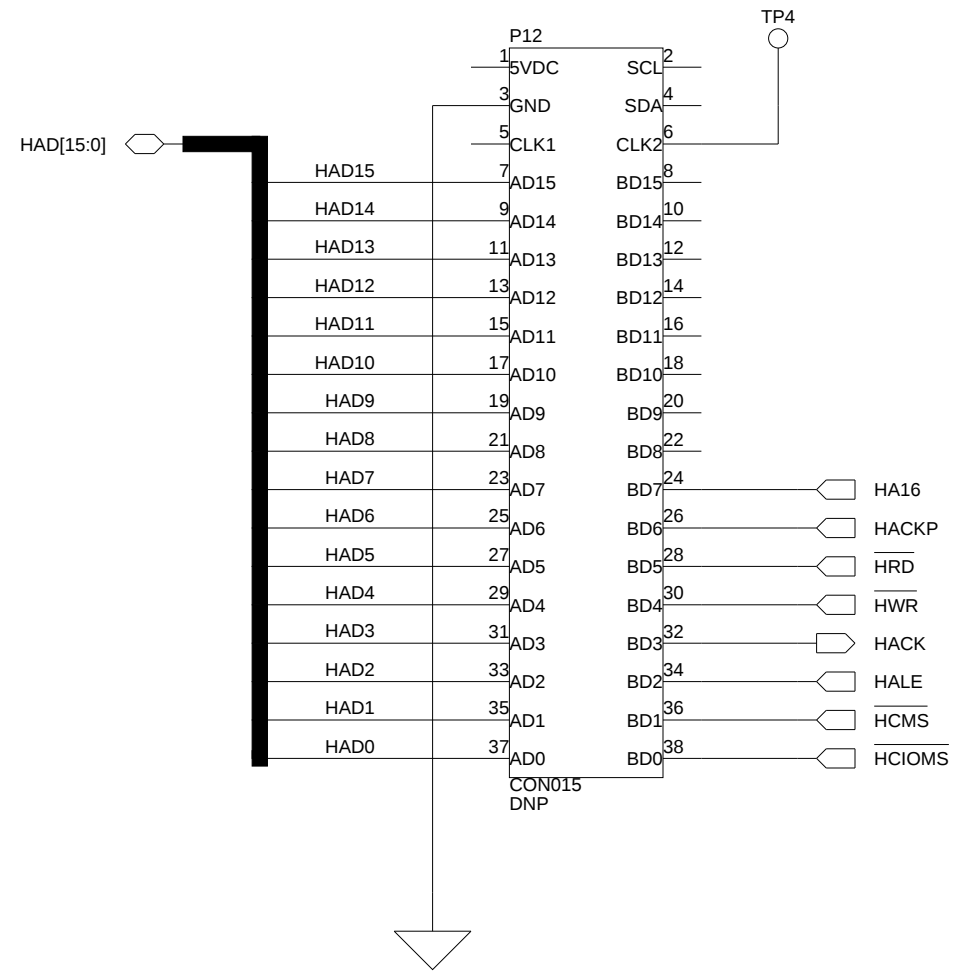
4



EXTERNAL MEMORY INTERFACE 1

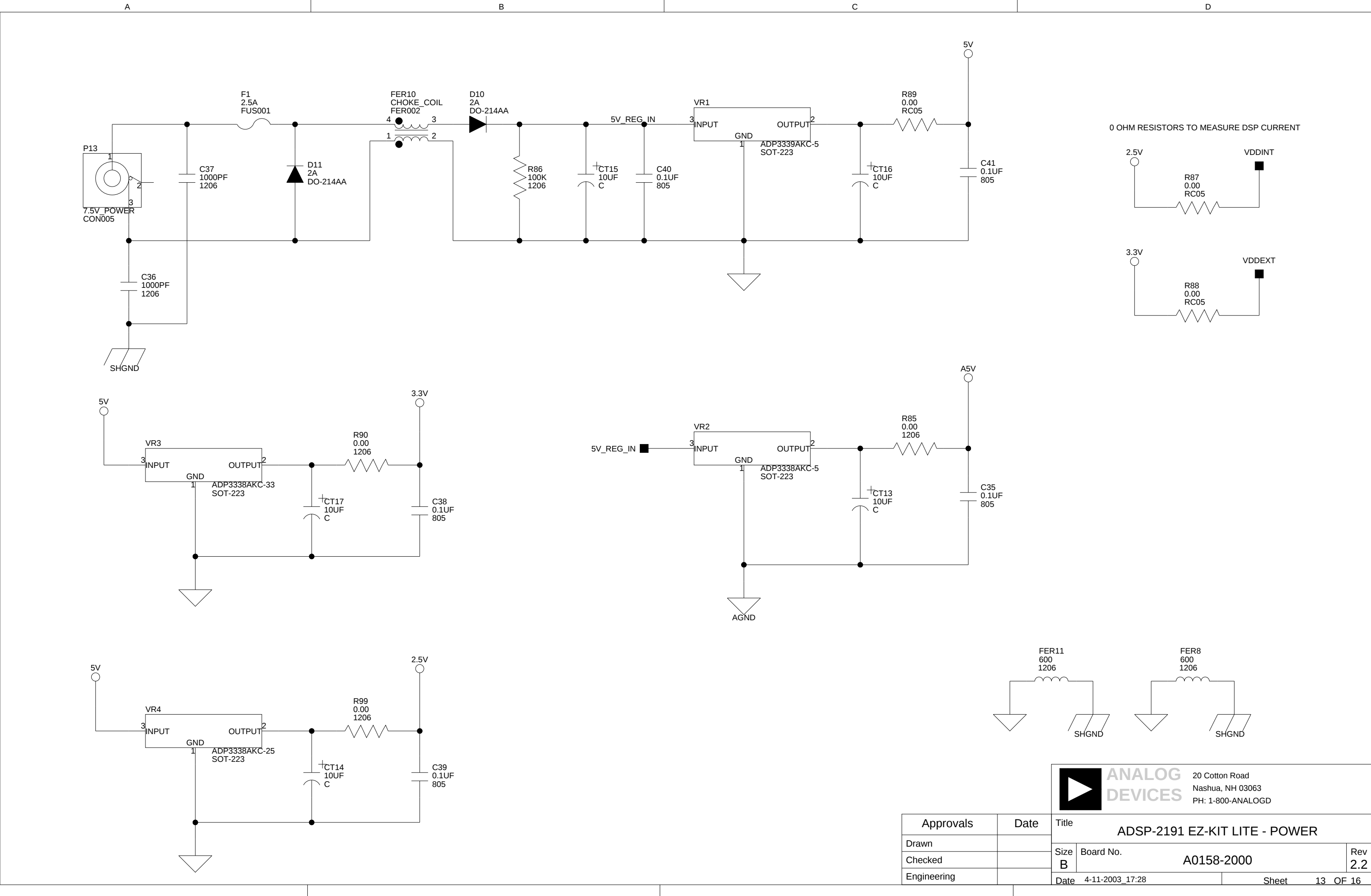


EXTERNAL MEMORY INTERFACE 2

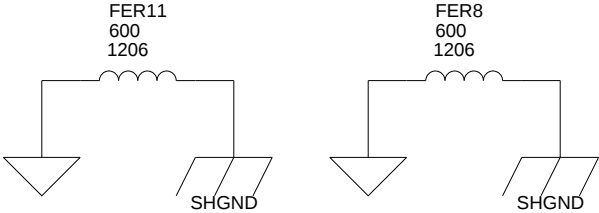
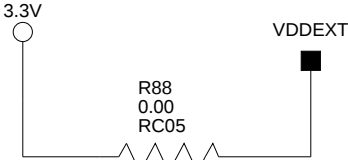
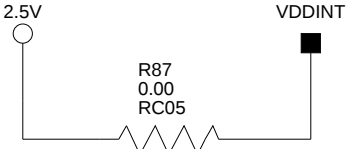


HOST PORT INTERFACE

 ANALOG DEVICES		20 Cotton Road Nashua, NH 03063 PH: 1-800-ANALOGD	
Approvals	Date	Title ADSP-2191 EZ-KIT LITE - CONNECTORS 2	
Drawn		Size B	Board No. A0158-2000
Checked		Rev 2.2	
Engineering		Date 7-29-2003_14:54	Sheet 12 OF 16



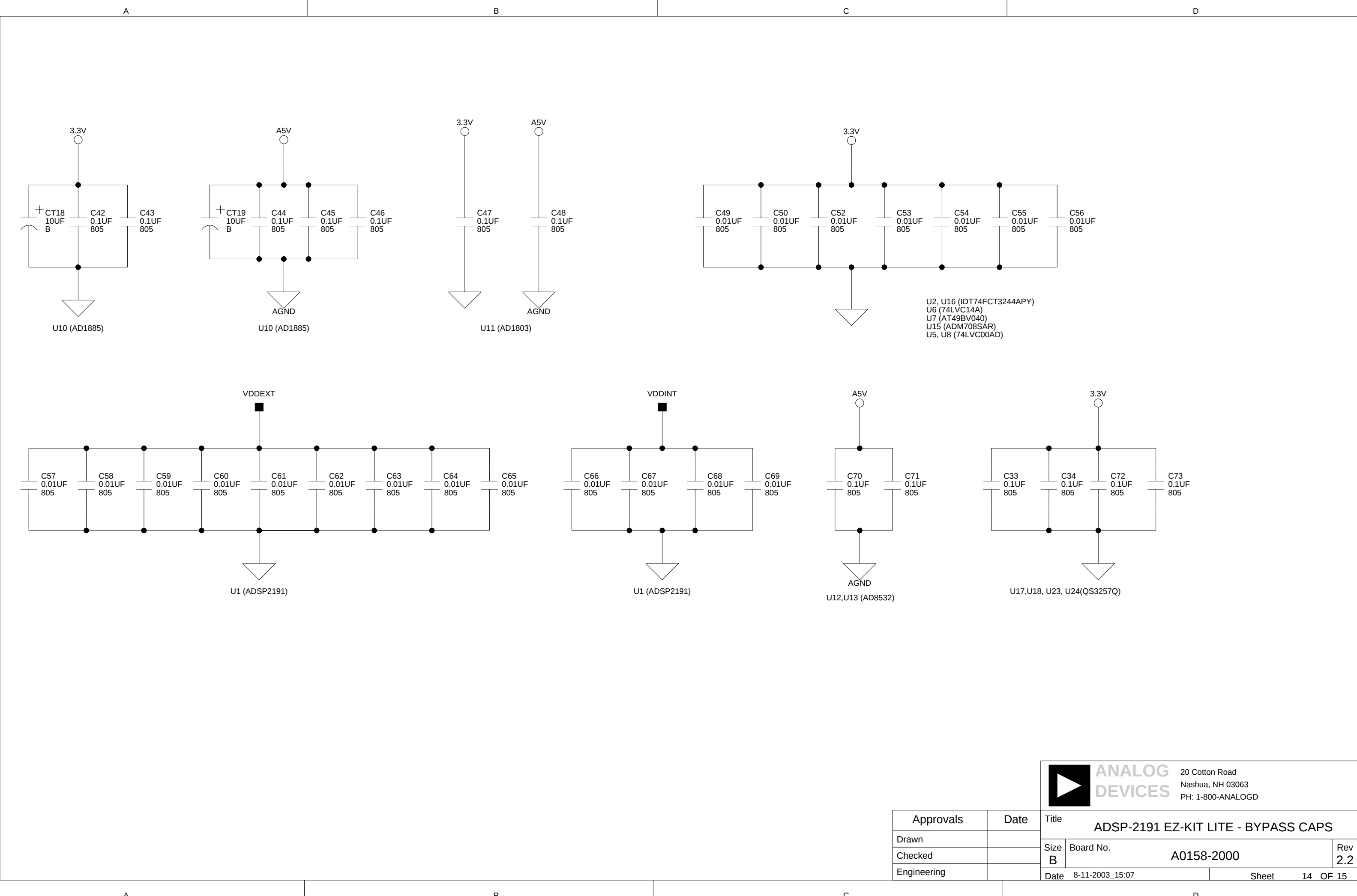
0 OHM RESISTORS TO MEASURE DSP CURRENT



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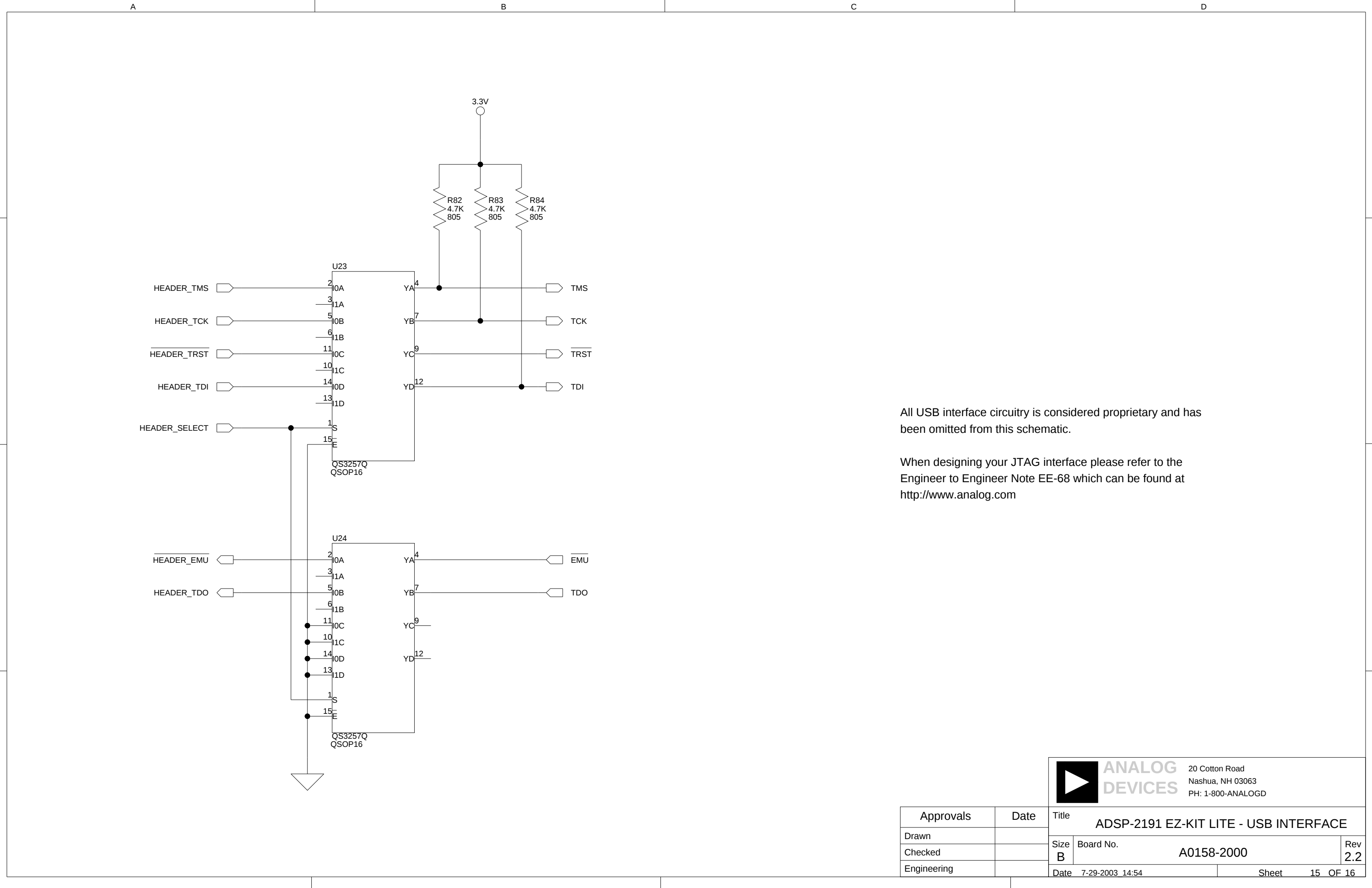
Approvals	Date	Title			
Drawn		ADSP-2191 EZ-KIT LITE - POWER			
Checked		Size B	Board No. A0158-2000		Rev 2.2
Engineering		Date	4-11-2003 17:28		Sheet 13 OF 16



**ANALOG
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20 Cotton Road
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Approvals	Date	Title			
Drawn		ADSP-2191 EZ-KIT LITE - BYPASS CAPS			
Checked		Size B	Board No. A0158-2000		Rev 2.2
Engineering		Date	8-11-2003 15:07		Sheet 14 OF 15



All USB interface circuitry is considered proprietary and has been omitted from this schematic.

When designing your JTAG interface please refer to the Engineer to Engineer Note EE-68 which can be found at <http://www.analog.com>



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Approvals		Date	Title		
Drawn			ADSP-2191 EZ-KIT LITE - USB INTERFACE		
Checked			Size B	Board No. A0158-2000	Rev 2.2
Engineering					
			Date	7-29-2003 14:54	Sheet 15 OF 16