

The schematic diagram shows a SAW-FILTER (F1) with a frequency of 350MHz. The input is labeled 350MHz_CLKGEN_OUT and is connected to pin 2. The output is labeled 350MHz_CLKGEN_OUT_FILTERED and is connected to pin 6. The filter is powered by a 3.3V supply (pin 1) and ground (pin 3). A JP13 jumper is connected between pins 2 and 6. The filter is also connected to ground at pins 4, 5, and 7.

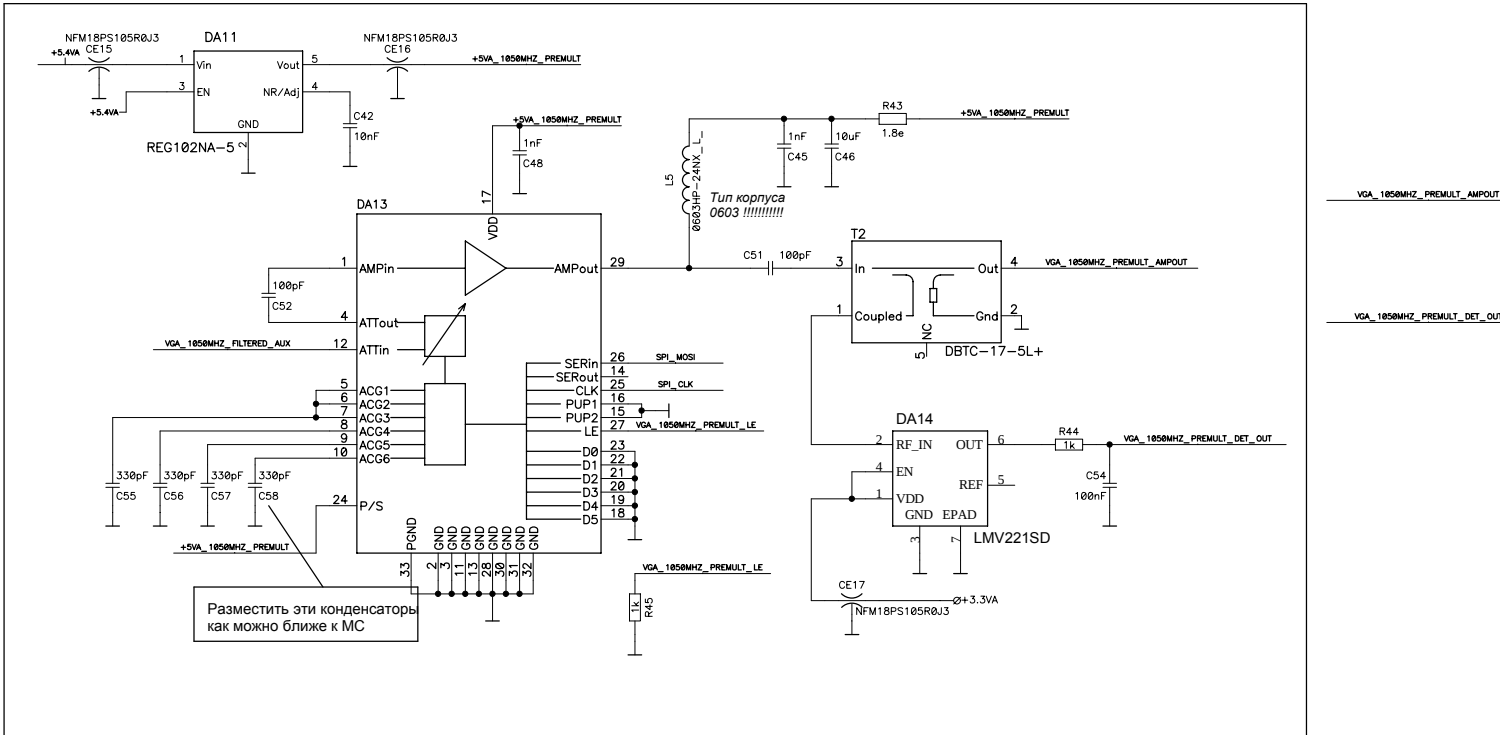
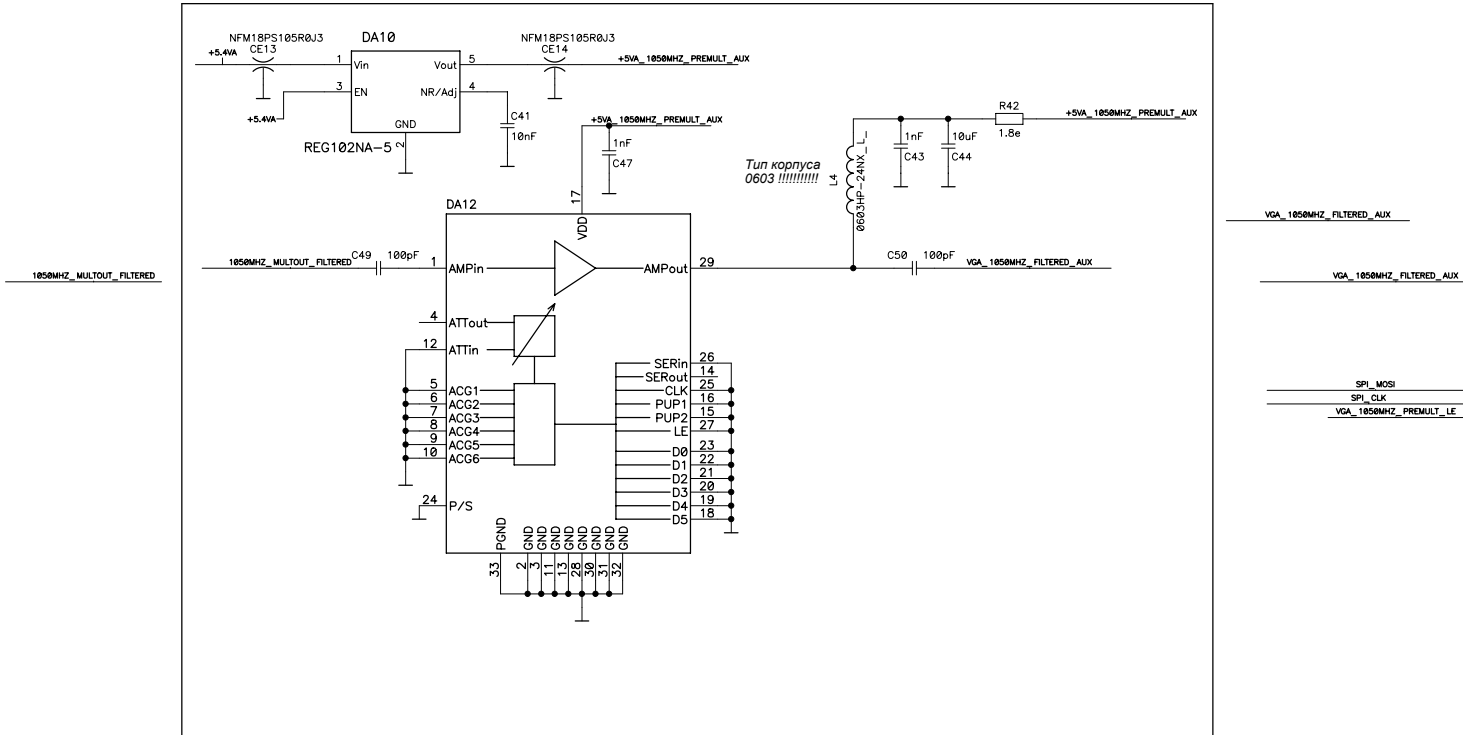
Разместить эти конденсаторы как можно ближе к МС

The diagram shows three alternative filter configurations for the 1850MHz_MULTOUT signal path, controlled by a jumper labeled "Перемычку провязать под микросхемой" (Solder the jumper under the microchip).

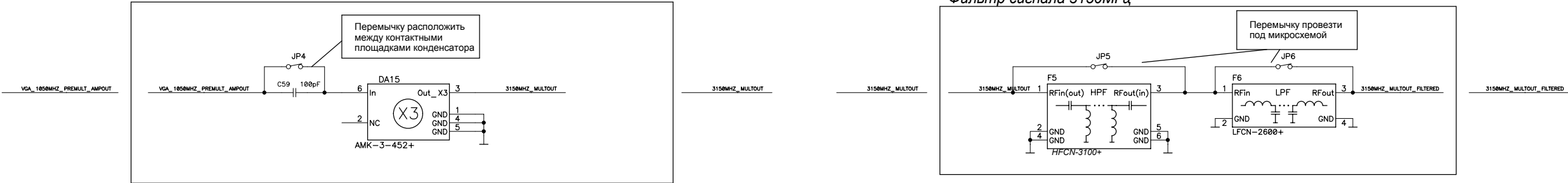
- Option JP1:** Uses the **LFCN-900D+** filter. The signal path is 1850MHz_MULTOUT (pin 1) → RFIn (pin 2) → LPF (pin 3) → RFout (pin 4) → GND (pin 5).
- Option JP2:** Uses the **SAW-FILTER 1047.65MHz** (B3852). The signal path is 1850MHz_MULTOUT (pin 1) → In (pin 2) → Out (pin 3) → GND (pin 4) → GND (pin 5).
- Option JP3:** Uses the **HFCN-880+** filter. The signal path is 1850MHz_MULTOUT (pin 1) → RFIn(out) (pin 2) → HPF (pin 3) → RFout(in) (pin 4) → 1850MHz_MULTOUT_FILTERED (pin 5).

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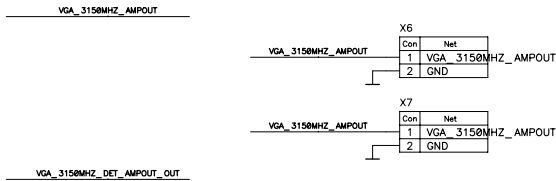
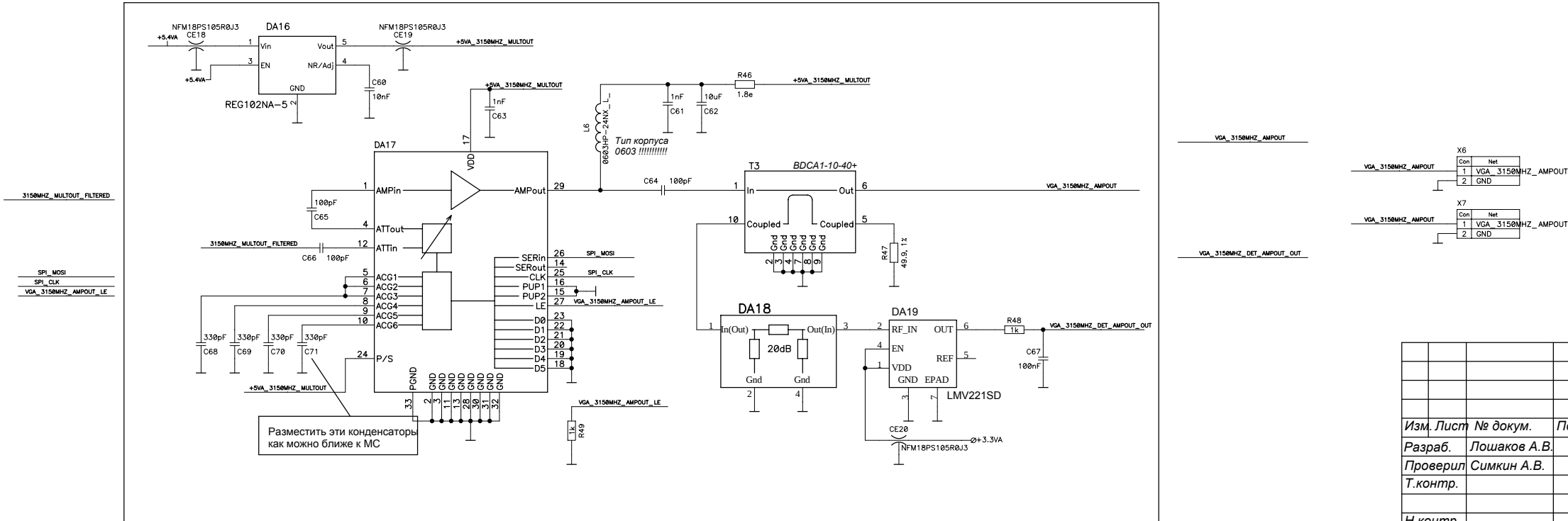
Усилитель сигнала 1050МГц, перед его умножением
 Ответитель усиленного сигнала 1050МГц
 Дектор выходной мощности сигнала



Умножитель X3 для получения 3150МГц



Усилитель выходного сигнала 3150МГц
 Ответитель усиленного выходного сигнала 3150МГц
 Дектор выходной мощности сигнала

[illegible]